

## INFORMATION DISCLOSURE

ATTY. DOCKET NO.

SERIAL NO.

CITATION

2380-187

09/829,451

APPLICANT

ANDERSSON et al

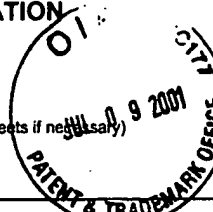
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## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
CUD	5,710,882	1-1998	Svennevik et al.			
CUD	5,809,129	9-1998	Andersson et al.			
CUD	6,128,295	10-2000	Larsson et al.			
CUD	5,790,541	8-1998	Patrick et al.			
CUD	5,491,693	2-1996	Britton et al.			
CUD	5,987,515	11-1999	Ratcliff et al.			
CUD	6,009,103	12-1999	Woundy			
CUD	5,963,553	10-1999	Wicklund			
CUD	6,088,359	7-2000	Wicklund et al.			
CUD	6,154,459	11-2000	Wicklund			
CUD	6,034,958	3-2000	Wicklund			
CUD	5,963,564	10-1999	Petersen et al.			
CUD	5,946,309	8-1999	Westberg et al.			
CUD	5,467,347	11-1995	Petersen			
CUD	5,361,257	11-1994	Petersen			
CUD	5,623,493	4/1997	Kagemoto			
CUD	5,570,362	10-1996	Nishimura			
CUD	4,692,917	9-1987	Fujioka			
CUD	4,973,956	11-1990	Lin et al.			
CUD	5,038,343	8-1991	Lebizay et al.			
CUD	5,079,762	1-1992	Tanabe			
CUD	5,126,999	6-1992	Munter et al.			
CUD	5,128,931	7-1992	Yamanaka et al.			
CUD	5,140,582	8-1992	Tsuboi et al.			
CUD	5,144,293	9-1992	Rouse			
CUD	5,680,390	10-1997	Robrock, II			
CUD	5,150,358	9-1992	Punj et al.			

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DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
CUD	97/41696	11-1997	WO		
CUD	0 800 324 A2	10-1997	EP		

## OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

CUD	"ATM User-network Interface (UNI) Signaling Specification", Version 4.0, af-sig-0061.00, July 1996, generated by the ATM Forum Technical Committee
CUD	ATM Adaptation Layer Switching, Mauger et al, XVI World Telecom Congress Proceedings (XP-000720525) (pp. 207-214)
*Examiner	<i>[Signature]</i> Date Considered 2/7/05

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

Form PTO-FB-A820 (Also PTO-1449)

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CW	5,724,348	3-1998	Basso et al.			
CW	5,499,239	3-1996	Munter			
CW	5,740,156	4-1998	Tanabe et al.			

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DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
					YES	NO
CW	0 405 042 A1	1-1991	EP			
CW	95/30318	11-1995	WO			
CW	97/33406	12-1997	WO		partial	
CW	0 827 305 A1	3-1998	EP			
CW	93/19559	9-1993	WO			
CW	96/34478	10-1996	WO			

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, etc.)

CW	U.S. Patent Application Serial No. 09/188,101, filed November 9, 1998, "Asynchronous Transfer Mode"
CW	U.S. Patent Application Serial No. 09/188,102, filed November 9, 1998, Asynchronous Transfer Mode System Handling Differing AAL Protocol"
CW	U.S. Patent Application Serial No. 09/249,785, filed February 19, 1999, entitled "Establishing Internal Control Paths In ATM Node"
CW	Mauger et al, "QoS Guarantees for Multimedia Services on a TDMA-Based Satellite Network", IEEE Communications Magazine, July 1997, pgs. 60-65
CW	DOI et al, "A High-Speed ATM Switch Architecture for FTTH - An ATM Switch Architecture with Input and Cross-Point Buffers", ISS '95, World Telecommunications Congress (International Switching Symposium), Advanced Switching Technologies for Universal Telecommunications at the Beginning of the 21 <sup>st</sup> Century, Berlin, April 23-28, 1995, vol. 1, no. SYMP 15, 23 April 1995, pgs. 384-388
CW	Weller et al., "Scheduling Nonuniform Traffic in a Packet Switching System with Small Propagation Delay", Proceedings of the Conference on Computer Communications (INFOCOM), Toronto, June 12-16, 1994, vol. 3, 12 June 1994, pgs. 1344-1351
CW	Eneroth et al, "ATM Transport in Cellular Networks", Iss '97, World Telecommunications Congress (International Switching Symposium), Global Network Evolution: Convergence or Collision? Toronto, Sept. 21-26, 1997, vol. 2, 21 September 1997, pgs. 139-146
CW	Saito et al, "Layered Cell Structure for ATM Networks", 1996 IEEE International Conference on Communications (ICC), Converging Technologies for Tomorrow's Applications, Dallas, June 23-27, 1996, vol. 3, 23 June 1996, pgs. 1254-1257
CW	McTiffin et al, "Mobile Access to an ATM Network Using a CDMA Air Interface", IEEE Journal on Selected Areas in Communications, vol. 12, no. 5, 1 June 1994, pgs. 900-908
CW	Baldwin et al, "AAL2 - A new ATM Adaptation Layer for Small Packet Encapsulation and Multiplexing", Bell Labs Technical Journal, vol. 2, no. 2, 21 March 1997, pgs. 111-131

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CID	5,568,479	10-1996	Watanabe et al.			

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CID	Stephens et al, "Large-Scale ATM Switching Systems for B-ISDN", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1157-1160
CID	Chao, "A Recursive Modular Terabit/Second ATM Switch", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1161-1172
CID	Tobagi et al, "Architecture, Performance, and Implementation of the Tandem Banyan Fast Packet Switch" IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1173-1193
CID	Urushidani, "Rerouting Network: A High-Performance Self-Routing Switch for B-ISDN", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1194-1204
CID	Yang et al., "A Reconfigurable ATM Switch Fabric for Fault Tolerance and Traffic Balancing", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1205-1217
CID	Itoh, "A Fault-Tolerant Switching Network for B-ISDN", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1218-1226
CID	Banwell et al., "Physical Design Issues for Very Large ATM Switching Systems", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1227-1238
CID	Kozaki et al, "32X32 Shared Buffer Type ATM Switch VLSI's for B-ISDN's", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1239-1247
CID	Shobatake et al, "A One-Chip Scalable 8 * 8 ATM Switch LSI Employing Shared Buffer Architecture", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1248-1254
CID	Banniza et al, "Design and Technology Aspects of VLSI's for ATM Switches", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1255-1264
CID	Katevenis et al, "Weighted Round-Robin Cell Multiplexing in a General-Purpose ATM Switch Chip", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1265-1279
CID	Giapopelli et al, "Sunshine: A High-Performance Self-Routing Broadband Packet Switch Architecture", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1289-1298
CID	Fischer et al, "A Scalable ATM Switching System Architecture", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1299-1307
CID	Matsunaga et al, "A 1.5 Gb/s 8X8 Cross-Connect Switch Using a Time Reservation Algorithm" IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1308-1317
CID	Schroeder et al, "Autonet: A High-Speed, Self-Configuring Local Area Network Using Point-to-Point Links", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1318-1335
CID	Stavarakakis, "Efficient Modeling of Merging and Splitting Processes in Large Networking Structures", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1336-1347
CID	Cisneros et al, "A Large ATM Switch Based on Memory Switches and Optical Star Couplers", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1348-1360
CID	Itoh et al, "Practical Implementation and Packaging Technologies for a Large-Scale ATM Switching Systems", IEEE Journal, vol. 9, no. 8, Oct. 1991, pp. 1280-1288

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22	Veeraraghavan et al, "Parallel Connection Control (PCC) Algorithm for ATM Networks", 1996 IEEE Int'l. Conference on Communications (ICC), Converging Technologies for Tomorrow's applications, Dallas, June 23-27, 1996, New York, vol. 3, 23 June 1996, pp. 1635-1641
23	Fritz, "Demystifying ATM Addressing", BYTE, McGraw-Hill Inc., St. Petersburg, US, vol. 22, no. 12, December 1, 1997, pp. 57-58

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## TRANSLATION

<div style="border: 1px solid black; border-radius: 50%; width: 40px; height: 40px; display: flex; align-items: center; justify-content: center; margin: 5px;"> <div style="border: 1px solid black; width: 20px; height: 20px; margin: 2px;"></div> </div>	International Preliminary Examination mailed January 28, 2003 in corresponding PCT Application No. PCT/SE01/02819	
*Examiner		Date Considered <u>2/7/05</u>

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